

ABSTRACT OF THE DISCLOSURE

A display 1 has two display panels 2, 3 each including an active matrix substrates 7, 8 including: source bus lines 4, 5 and gate bus lines 9 arranged to form a matrix; TFTs provided near respective intersections of the source bus lines 4, 5 and the gate bus lines 9; and pixel electrodes electrically connected to the source bus lines and the gate bus lines through the TFT. Of the source bus lines 4, 5, the source bus lines 5 are shared for use between the two active matrix substrates 7, 8. Meanwhile, the source bus lines 4 provided only to the active matrix substrate 7 have capacitances 6a, 6b formed thereon. Thus, the display with two display panels is prevented from developing block split and other display defects.